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PXIe-1073



Board Model Name **Board Part Number Range**
NI PXIe-1073 **196695X-01 Revisions G and earlier (where X is the revision number)**

Manufacturer: National Instruments

Volatile Memory

<u>Type</u> ¹	<u>Size</u>	<u>User Accessible/ System Accessible</u> ²	<u>Purpose</u>	<u>Method of Clearing</u> ³
Microcontroller	512 Bytes	No/No	Fan/Voltage/Temp Data Log	Power Cycle
FPGA	2910 Logic Elements	No/No	PCI, SMBus, LEDs state	Power Cycle

Non-Volatile Memory

<u>Type</u>	<u>Size</u>	<u>User Accessible/ Definable</u>	<u>Purpose</u>	<u>Method of Clearing</u>
Microcontroller	8 KB	No/No	System Monitor and Fan Control Code	None
EEPROM	1 Mbit	No/No	PCI Bridge settings and FPGA image	None
EEPROM	256 Kbit	No/No	PCI Express Switch settings	None
EEPROM	2 Kbit	No/No	CPCI Express Backplane Descriptor	None
CPLD	570 Logic Elements	No/No	Configuration Memory for Clocking	None

Media Storage

<u>Type</u>	<u>Size</u>	<u>User Accessible/Definable</u>	<u>Purpose</u>	<u>Method of Clearing</u>
NONE				

¹ Calibration constants that are stored in device EEPROMs include information for the device's full operating range and do not maintain any unique data for specific frequencies at which the device is used.

² Items above that are noted as **No** for User Accessible/System Accessible are for the following reason(s): Hardware changes or a unique software tool from National Instruments are required to modify contents of the memory listed. This software tool is not distributed to public users for any personal access or customization; also known as non-normal use.

³ The designation *None Available to User* indicates that the ability to clear this memory is not available to the user under normal operation. The utilities required to perform this action are not distributed by National Instruments to customers for normal use.

Terms and Definitions

User Accessible The user can directly write or modify the contents of the memory during normal instrument operation.

System Accessible Any data that can access, change or modify the memory. This could be something that is not deliberate by the user and could be a background driver implementation, such as storing application information in RAM to increase speed of use.

Cycle Power This defined the process of completely removing power from the device and its components. This includes a complete shutdown of the PC or Chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory Volatile memory requires power to maintain the stored information. When power is removed from this memory its contents are lost.

Non-Volatile Non-volatile memory will retain its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.