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PXIe-5652

Manufacturer: National Instruments

Assembly Part Numbers: PXIe-5673 (RF Vector Signal Generator)

Part Number	Description
780418-12	NI PXIe-5673 6.6 GHZ RF VECTOR SIGNAL GENERATOR (ENHANCED IQ MODULATOR) WITH 512 MB RAM
781261-01	NI PXIe-5673E 1.3 GHZ RF VECTOR SIGNAL GENERATOR W/ 128 MB RAM
781262-01	NI PXIe-5673E 3.3 GHZ RF VECTOR SIGNAL GENERATOR W /128 MB RAM
781263-01	NI PXIe-5673E 6.6 GHZ RF VECTOR SIGNAL GENERATOR W/ 128 MB RAM
781261-02	NI PXIe-5673E 1.3 GHZ RF VECTOR SIGNAL GENERATOR W/ 512 MB RAM
781263-02	NI PXIe-5673E 6.6 GHZ RF VECTOR SIGNAL GENERATOR W/ 512 MB RAM
781340-03	NI PXIe-5673E 6.6 GHZ 3-CHANNEL PHASE-COHERENT MIMO RF VECTOR SIGNAL GENERATOR 512 MB RAM PER CH
781340-04	NI PXIe-5673E 6.6 GHZ 4-CHANNEL PHASE-COHERENT MIMO RF VECTOR SIGNAL GENERATOR 512 MB RAM PER CH
780567-02	PXIe-5673/5673E MIMO CABLE KIT
780416-02	NI PXIe-5673 1.3 GHZ RF VECTOR SIGNAL GENERATOR WITH 512 MB RAM
781340-02	NI PXIe-5673E 6.6 GHZ 2-CHANNEL PHASE-COHERENT MIMO RF VECTOR SIGNAL GENERATOR 512 MB RAM PER CH
781263-03	NI PXIe-5673E 6.6 GHZ RF VECTOR SIGNAL GENERATOR W/ 2 GB RAM
780567-01	PXIe-5673/5673E REPLACEMENT CABLE KIT
780417-01	NI PXIe-5673 3.3 GHZ RF VECTOR SIGNAL GENERATOR WITH 128 MB RAM
780418-01	NI PXIe-5673 6.6 GHZ RF VECTOR SIGNAL GENERATOR WITH 128 MB RAM
780417-02	NI PXIe-5673 3.3 GHZ RF VECTOR SIGNAL GENERATOR WITH 512 MB RAM
780418-02	NI PXIe-5673 6.6 GHZ RF VECTOR SIGNAL GENERATOR WITH 512 MB RAM
780485-02	NI PXIe-5673 6.6 GHZ MIMO EXTENSION KIT WITH 128 MB RAM
781262-02	NI PXIe-5673E 3.3 GHZ RF VECTOR SIGNAL GENERATOR W/ 512 MB RAM
780485-01	NI PXIe-5673 6.6 GHZ MIMO EXTENSION KIT WITH 512 MB RAM
780416-01	NI PXIe-5673 1.3 GHZ RF VECTOR SIGNAL GENERATOR WITH 128 MB RAM
780485-11	NI PXIe-5673 6.6 GHZ MIMO EXTENSION KIT (ENHANCED IQ MODULATOR) WITH 512 MB RAM

Volatile and Non-Volatile Memory

This device is composed of four (4) independent hardware models. Refer to the Letter of Volatility for each individual model listed below by going to ni.com/info and typing in the appropriate Info Code.

Model and Description	Info Code
NI PXIe-5611, MODULE ASSEMBLY, IQ MODULATOR	exw6dm
NI PXIe-5450, MODULE ASSEMBLY, 512MB MEMORY	ex5v39
NI PXIe-5450, MODULE ASSEMBLY, 128MB MEMORY	ex5v39
NI PXIe-5450, MODULE ASSEMBLY, 2GB MEMORY	ex5v39

NI PXIE-5451, MODULE ASSEMBLY, 2GB MEMORY	exkq7z
NI PXI-5650, MODULE ASSEMBLY, 1.3 GHZ	ex88u7
NI PXIE-5651, MODULE ASSEMBLY, 3.3 GHZ	ex88u7
NI PXIE-5652, MODULE ASSEMBLY, 6.6 GHZ	ex88u7

Terms and Definitions

Cycle Power:

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory:

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

Non-Volatile Memory:

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

User Accessible:

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

System Accessible:

The component is read and/or write addressable from the host without the need to physically alter the product.

Clearing:

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

Sanitization:

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.