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cRIO-9148



Board Part Number Range Board Model Name

907x & 9148 Series cRIO 779998-01, 780471-01, 779999-01, 781502-01

Manufacturer: National Instruments

Volatile Memory

| | | User Accessible/ | Battery | | |
|-------------------|----------|--------------------------------|---------|---------|--------------------|
| Type ¹ | Size | System Accessible ² | Backup? | Purpose | Method of Clearing |
| (9072/9073/91 | 48) | ' | | | |
| DRAM | 64 MB | Yes/Yes | | RAM | Cycle Power |
| (9074) | | | | | • |
| DRAM | 128 MB | Yes/Yes | | RAM | Cycle Power |
| (9072) | | | | | |
| FPGA RAM | XC3S1000 | Yes/Yes | | FPGA | Cycle Power |
| (9082) | | | | | |
| FPGA RAM | XC3S2000 | Yes/Yes | | FPGA | Cycle Power |
| Non-Volatile | Memory | | | | |
| | · | User Accessible/ | Battery | | |
| Type | Size | System Accessible | Backup? | Purpose | Method of Clearing |

| Type | Size | System Accessible | Backup? | Purpose | Method of Clearing |
|-----------------------------------|------------|-------------------|---------|--|--|
| Watchdog RTC | | No/No | | Real-Time Clock | None Available to User |
| (9074) SSD (9072/9073/914 | 128 MB | Yes/Yes | | Primary Storage | Cleared by Reformatting |
| SSD | 128 MB | Yes/Yes | | Primary Storage | Cleared by Reformatting |
| Flash Flash | 2M x 8-bit | No/No Yes/Yes | | Firmware BSP Firmware, FPGA Startup | In System Not Available/Clear Startup |
| (9074) Flash Ethernet Flash | | No/No No/No | | Arbiter Ethernet Controller | None Available to User None Available to User |
| Media Storag | e | | | | |

User Accessible/ **Battery Type** Size **System Accessible** Backup? Purpose **Method of Clearing**

NONE

Edition Date: March 2016 NI Part Number: 375897A-01

¹ Calibration constants that are stored in device EEPROMs include information for the device's full operating range and does not maintain any unique data for specific frequencies at which the device is used.

² Items above that are noted as **No** for User Accessible/System Accessible are for the following reason(s):

Hardware changes or a unique software tool from National Instruments are required to modify contents of the memory listed. This software tool is not distributed to public users for any personal access or customization; also known as non-normal use.

³ The designation None Available to User indicates that the ability to clear this memory is not available to the user under normal operation. The utilities required to perform this action are not distributed by National Instruments to customers for normal use.



Terms and Definitions

User Accessible The user can directly write or modify the contents of the memory during normal instrument operation.

System Accessible Any data that can access, change or modify the memory. This could be something that is not deliberate by the user and could be a background driver implementation, such as storing application information in RAM to increase speed of use.

Cycle Power This defined the process of completely removing power from the device and its components. This includes a complete shutdown of the PC or Chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory Volatile memory requires power to maintain the stored information. When power is removed from this memory its contents are lost.

Non-Volatile Non-volatile memory will retain its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.

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