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**PXI-2543**

**Manufacturer:** National Instruments

**Board Assembly Part Numbers** (Refer to Procedure 1 for identification procedure):

Part Number and Revision	Description
153939A-01L or later	PXI-2542
152025A-01L or later	PXI-2543
153941A-01L or later	PXI-2544
153940A-01L or later	PXIe-2542
152024A-01L or later	PXIe-2543
153942A-01L or later	PXIe-2544

### Volatile Memory

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User<sup>1</sup> Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Relay scanlists	SRAM	128 KB	No	No	Yes	Cycle Power
Relay counts, states	FPGA Block RAM	150 KB	No	No	Yes	Cycle Power
Relay Drivers	FPGA LUT RAM	2Kb	No	No	Yes	Cycle Power

### Non-Volatile Memory (*incl. Media Storage*)

<i>Target Data</i>	<i>Type</i>	<i>Size</i>	<i>Battery Backup</i>	<i>User Accessible</i>	<i>System Accessible</i>	<i>Sanitization Procedure</i>
Device configuration	FLASH		No	No	Yes	None
<ul style="list-style-type: none"> <li>• PCI configuration data, and serial number</li> <li>• Relay module config, and relay cycle counts</li> </ul>		4Mb 4 Mb				
Relay drivers	CPLD	256 LUTs	No	No	Yes	None

<sup>1</sup> Refer to *Terms and Definitions* section for clarification of *User* and *System Accessible*

## Procedures

### **Procedure 1 –Board Assembly Part Number Identification:**

To determine the Board Assembly Part Number and Revision, refer to the label applied to the surface of your product. The Assembly Part Number should be formatted as “P/N: #####A-##L

## Terms and Definitions

### **Cycle Power:**

The process of completely removing power from the device and its components and allowing for adequate discharge. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

### **Volatile Memory:**

Requires power to maintain the stored information. When power is removed from this memory, its contents are lost. This type of memory typically contains application specific data such as capture waveforms.

### **Non-Volatile Memory:**

Power is not required to maintain the stored information. Device retains its contents when power is removed. This type of memory typically contains information necessary to boot, configure, or calibrate the product or may include device power up states.

### **User Accessible:**

The component is read and/or write addressable such that a user can store arbitrary information to the component from the host using a publicly distributed NI tool, such as a Driver API, the System Configuration API, or MAX.

### **System Accessible:**

The component is read and/or write addressable from the host without the need to physically alter the product.

### **Clearing:**

Per *NIST Special Publication 800-88 Revision 1*, “clearing” is a logical technique to sanitize data in all User Accessible storage locations for protection against simple non-invasive data recovery techniques using the same interface available to the user; typically applied through the standard read and write commands to the storage device.

### **Sanitization:**

Per *NIST Special Publication 800-88 Revision 1*, “sanitization” is a process to render access to “Target Data” on the media infeasible for a given level of effort. In this document, clearing is the degree of sanitization described.