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**PXI-6250**

## DEVICE SPECIFICATIONS

# NI 6250

M Series Data Acquisition: 16 AI, 1.25 MS/s, 24 DIO

The following specifications are typical at 25 °C, unless otherwise noted. For more information about the NI 6250, refer to the *M Series User Manual* available at [ni.com/manuals](http://ni.com/manuals).

## Analog Input

Number of channels	8 differential or 16 single ended
ADC resolution	16 bits
DNL	No missing codes guaranteed
INL	Refer to the <i>AI Absolute Accuracy</i> section
Sample rate	
Single channel maximum	1.25 MS/s
Multichannel maximum (aggregate)	1.00 MS/s
Minimum	No minimum
Timing resolution	50 ns
Timing accuracy	50 ppm of sample rate
Input coupling	DC
Input range	$\pm 0.1$ V, $\pm 0.2$ V, $\pm 0.5$ V, $\pm 1$ V, $\pm 2$ V, $\pm 5$ V, $\pm 10$ V
Maximum working voltage for analog inputs (signal + common mode)	$\pm 11$ V of AI GND
CMRR (DC to 60 Hz)	100 dB
Input impedance	
Device on	
AI+ to AI GND	$>10$ G $\Omega$ in parallel with 100 pF
AI- to AI GND	$>10$ G $\Omega$ in parallel with 100 pF

Device off	
AI+ to AI GND	820 $\Omega$
AI- to AI GND	820 $\Omega$
Input bias current	$\pm 100$ pA
Crosstalk (at 100 kHz)	
Adjacent channels	-75 dB
Non-adjacent channels	-95 dB
Small signal bandwidth (-3 dB)	1.7 MHz
Input FIFO size	4,095 samples
Scan list memory	4,095 entries
Data transfers	DMA (scatter-gather), interrupts, programmed I/O
Overvoltage protection for all analog input and sense channels	
Device on	$\pm 25$ V for up to four AI pins
Device off	$\pm 15$ V for up to four AI pins
Input current during overvoltage condition	$\pm 20$ mA maximum/AI pin

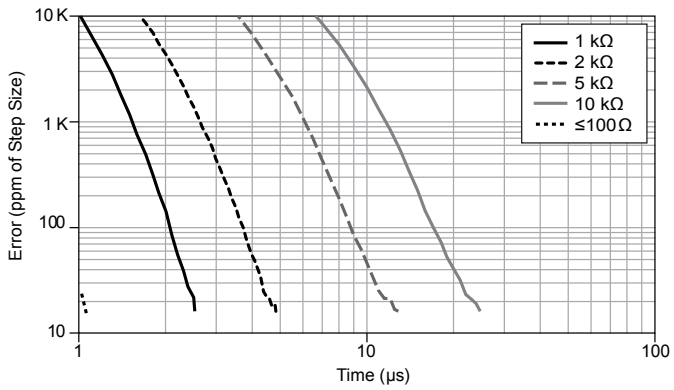
## Settling Time for Multichannel Measurements

**Table 1.** Settling Time for Multichannel Measurements

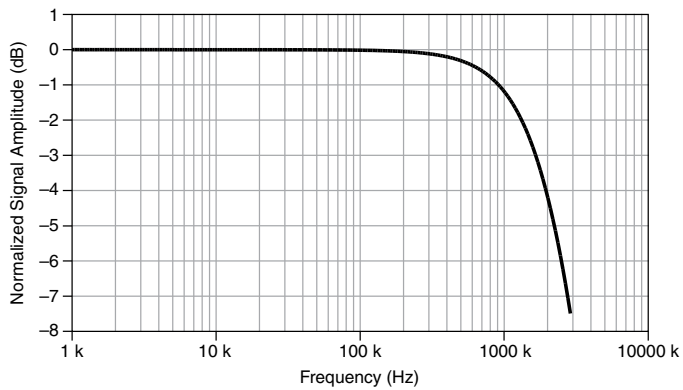
Range	$\pm 60$ ppm of Step ( $\pm 4$ LSB for Full-Scale Step)	$\pm 15$ ppm of Step ( $\pm 1$ LSB for Full-Scale Step)
$\pm 1$ V, $\pm 2$ V, $\pm 5$ V, $\pm 10$ V	1 $\mu$ s	1.5 $\mu$ s
$\pm 0.5$ V	1.5 $\mu$ s	2 $\mu$ s
$\pm 0.1$ V, $\pm 0.2$ V	2 $\mu$ s	8 $\mu$ s

# Typical Performance Graphs

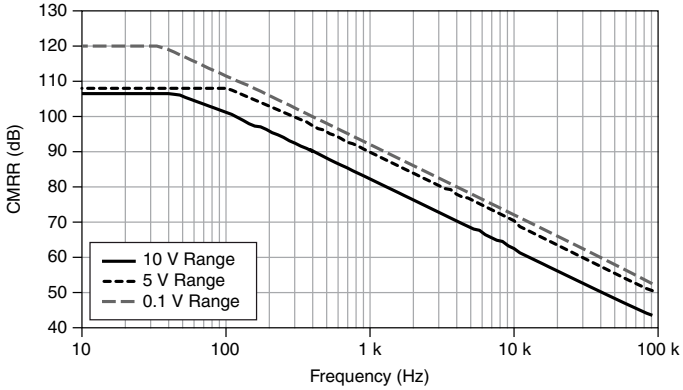
**Figure 1.** Settling Error versus Time for Different Source Impedances



**Figure 2.** AI Small Signal Bandwidth



**Figure 3. AI CMRR**



## AI Absolute Accuracy



**Note** Accuracies listed are valid for up to two years from the device external calibration.

**Table 2. AI Absolute Accuracy**

Nominal Range Positive Full Scale	Nominal Range Negative Full Scale	Residual Gain Error (ppm of Reading)	Residual Offset Error (ppm of Range)	Offset Tempco (ppm of Range/°C)	Random Noise, $\sigma$ ( $\mu$ Vrms)	Absolute Accuracy at Full Scale ( $\mu$ V)	Sensitivity ( $\mu$ V)
10	-10	60	20	21	280	1,920	112.0
5	-5	70	20	21	140	1,010	56.0
2	-2	70	20	24	57	410	22.8
1	-1	80	20	27	32	220	12.8
0.5	-0.5	90	40	34	21	130	8.4
0.2	-0.2	130	80	55	16	74	6.4
0.1	-0.1	150	150	90	15	52	6.0



**Note** Sensitivity is the smallest voltage change that can be detected. It is a function of noise.

Gain tempco	13 ppm/°C
Reference tempco	1 ppm/°C
INL error	60 ppm of range

## AI Absolute Accuracy Equation

$$\text{AbsoluteAccuracy} = \text{Reading} \cdot (\text{GainError}) + \text{Range} \cdot (\text{OffsetError}) + \text{NoiseUncertainty}$$

$$\text{GainError} = \text{ResidualAIGainError} + \text{GainTempco} \cdot (\text{TempChangeFromLastInternalCal}) + \text{ReferenceTempco} \cdot (\text{TempChangeFromLastExternalCal})$$

$$\text{OffsetError} = \text{ResidualAIOffsetError} + \text{OffsetTempco} \cdot (\text{TempChangeFromLastInternalCal}) + \text{INLError}$$

$$\text{NoiseUncertainty} = \frac{\text{Random Noise} \cdot 3}{\sqrt{100}} \text{ for a coverage factor of } 3 \sigma \text{ and averaging } 100 \text{ points.}$$

## AI Absolute Accuracy Example

Absolute accuracy at full scale on the analog input channels is determined using the following assumptions:

- TempChangeFromLastExternalCal = 10 °C
- TempChangeFromLastInternalCal = 1 °C
- number\_of\_readings = 100
- CoverageFactor = 3  $\sigma$

For example, on the 10 V range, the absolute accuracy at full scale is as follows:

$$\text{GainError} = 60 \text{ ppm} + 13 \text{ ppm} \cdot 1 + 1 \text{ ppm} \cdot 10 = 83 \text{ ppm}$$

$$\text{OffsetError} = 20 \text{ ppm} + 21 \text{ ppm} \cdot 1 + 60 \text{ ppm} = 101 \text{ ppm}$$

$$\text{NoiseUncertainty} = \frac{280 \mu\text{V} \cdot 3}{\sqrt{100}} = 84 \mu\text{V}$$

$$\text{AbsoluteAccuracy} = 10 \text{ V} \cdot (\text{GainError}) + 10 \text{ V} \cdot (\text{OffsetError}) + \text{NoiseUncertainty} = 1,920 \mu\text{V}$$

## Analog Triggers

Number of triggers	1
Source	AI <0..15>, APFI 0
Functions	Start Trigger, Reference Trigger, Pause Trigger, Sample Clock, Convert Clock, Sample Clock Timebase

Source level	
AI <0..15>	±Full scale
APFI 0	±10 V
Resolution	10 bits, 1 in 1,024
Modes	Analog edge triggering, analog edge triggering with hysteresis, and analog window triggering
Bandwidth (-3 dB)	
AI <0..15>	3.4 MHz
APFI 0	3.9 MHz
Accuracy	±1%
APFI 0 characteristics	
Input impedance	10 kΩ
Coupling	DC
Protection, power on	±30 V
Protection, power off	±15 V

## Digital I/O/PFI

### Static Characteristics

Number of channels	24 total, 8 (P0.<0..7>), 16 (PFI <0..7>/P1, PFI <8..15>/P2)
Ground reference	D GND
Direction control	Each terminal individually programmable as input or output
Pull-down resistor	50 kΩ typical, 20 kΩ minimum
Input voltage protection	±20 V on up to two pins <sup>1</sup>

### Waveform Characteristics (Port 0 Only)

Terminals used	Port 0 (P0.<0..7>)
Port/sample size	Up to 8 bits
Waveform generation (DO) FIFO	2,047 samples
Waveform acquisition (DI) FIFO	2,047 samples

<sup>1</sup> Stresses beyond those listed under *Input voltage protection* may cause permanent damage to the device.

DI Sample Clock frequency	0 MHz to 10 MHz, system and bus activity dependent
DO Sample Clock frequency	
Regenerate from FIFO	0 MHz to 10 MHz
Streaming from memory	0 MHz to 10 MHz, system and bus activity dependent
Data transfers	DMA (scatter-gather), interrupts, programmed I/O
DI or DO Sample Clock source <sup>2</sup>	Any PFI, RTSI, AI Sample or Convert Clock, Ctr <i>n</i> Internal Output, and many other signals

## PFI/Port 1/Port 2 Functionality

Functionality	Static digital input, static digital output, timing input, timing output
Timing output sources	Many AI, counter, DI, DO timing signals
Debounce filter settings	125 ns, 6.425 $\mu$ s, 2.56 ms, disable; high and low transitions; selectable per input

## Recommended Operating Conditions

Level	Minimum	Maximum
Input high voltage ( $V_{IH}$ )	2.2 V	5.25 V
Input low voltage ( $V_{IL}$ )	0 V	0.8 V
Output high current ( $I_{OH}$ ) P0.<0..7>	—	-24 mA
Output high current ( $I_{OH}$ ) PFI <0..15>/P1/P2	—	-16 mA
Output low current ( $I_{OL}$ ) P0.<0..7>	—	24 mA
Output low current ( $I_{OL}$ ) PFI <0..15>/P1/P2	—	16 mA

## Electrical Characteristics

Level	Minimum	Maximum
Positive-going threshold ( $V_{T+}$ )	—	2.2 V
Negative-going threshold ( $V_{T-}$ )	0.8 V	—

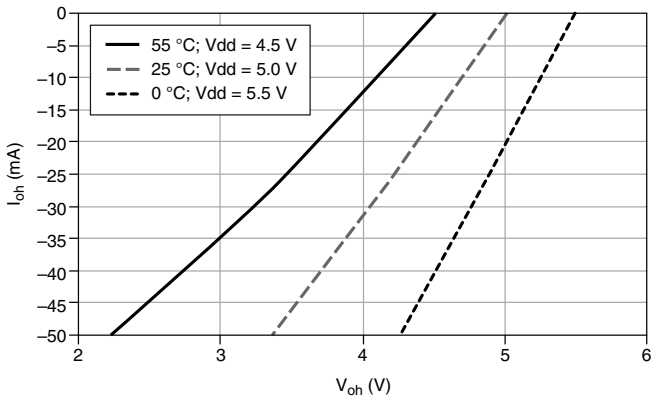
<sup>2</sup> The digital subsystem does not have its own dedicated internal timing engine. Therefore, a sample clock must be provided from another subsystem on the device or an external source.



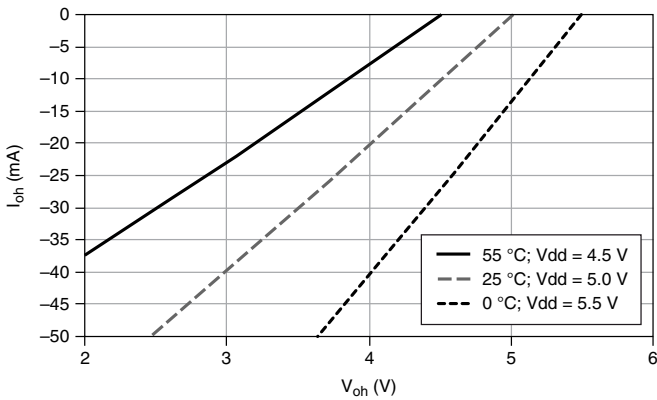
Level	Minimum	Maximum
Delta VT hysteresis ( $V_{T+} - V_{T-}$ )	0.2 V	—
$I_{IL}$ input low current ( $V_{in} = 0$ V)	—	-10 $\mu$ A
$I_{IH}$ input high current ( $V_{in} = 5$ V)	—	250 $\mu$ A

## Digital I/O Characteristics

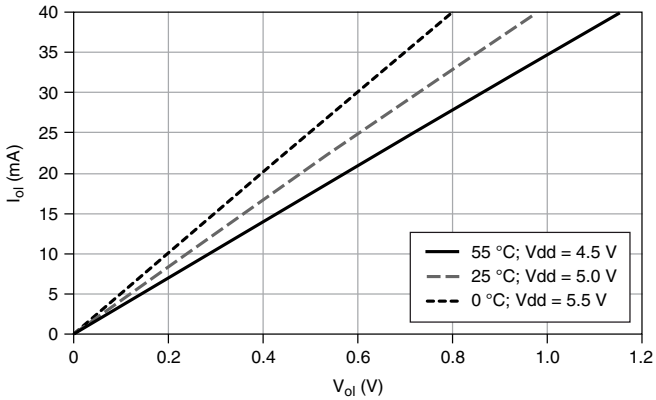
**Figure 4.** P0.<0..7>:  $I_{oh}$  versus  $V_{oh}$



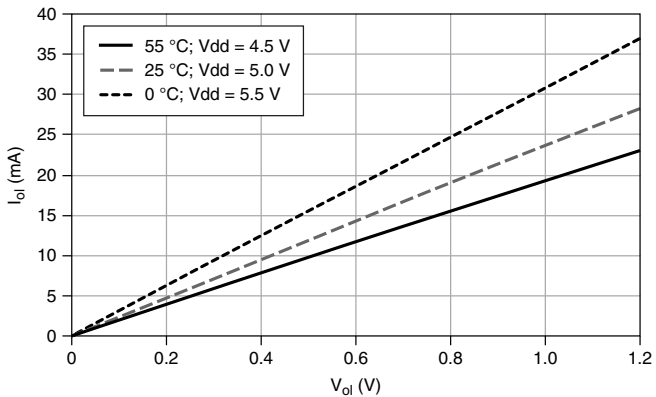
**Figure 5.** PFI <0..15>/P1/P2:  $I_{oh}$  versus  $V_{oh}$



**Figure 6.** P0.<0..7>:  $I_{ol}$  versus  $V_{ol}$



**Figure 7.** PFI <0..15>/P1/P2:  $I_{ol}$  versus  $V_{ol}$



## General-Purpose Counters/Timers

Number of counter/timers	2
Resolution	32 bits
Counter measurements	Edge counting, pulse, semi-period, period, two-edge separation
Position measurements	X1, X2, X4 quadrature encoding with Channel Z reloading; two-pulse encoding
Output applications	Pulse, pulse train with dynamic updates, frequency division, equivalent time sampling

Internal base clocks	80 MHz, 20 MHz, 0.1 MHz
External base clock frequency	0 MHz to 20 MHz
Base clock accuracy	50 ppm
Inputs	Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down
Routing options for inputs	Any PFI, RTSI, PXI_TRIG, PXI_STAR, analog trigger, many internal signals
FIFO	2 samples
Data transfers	Dedicated scatter-gather DMA controller for each counter/timer; interrupts; programmed I/O

## Frequency Generator

Number of channels	1
Base clocks	10 MHz, 100 kHz
Divisors	1 to 16
Base clock accuracy	50 ppm

Output can be available on any output PFI or RTSI terminal.

## Phase-Locked Loop (PLL)

Number of PLLs	1
Reference signal	PXI_STAR, PXI_CLK10, RTSI <0..7>
Output of PLL	80 MHz Timebase; other signals derived from 80 MHz Timebase including 20 MHz and 100 kHz Timebases

## External Digital Triggers

Source	Any PFI, RTSI, PXI_TRIG, PXI_STAR
Polarity	Software-selectable for most signals
Analog input function	Start Trigger, Reference Trigger, Pause Trigger, Sample Clock, Convert Clock, Sample Clock Timebase

Counter/timer function	Gate, Source, HW_Arm, Aux, A, B, Z, Up_Down
Digital waveform generation (DO) function	Sample Clock
Digital waveform acquisition (DI) function	Sample Clock

## Device-to-Device Trigger Bus

PCI	RTSI <0..7> <sup>3</sup>
PXI	PXI_TRIG <0..7>, PXI_STAR
Output selections	10 MHz Clock, frequency generator output, many internal signals
Debounce filter settings	125 ns, 6.425 $\mu$ s, 2.56 ms, disable; high and low transitions; selectable per input

## Bus Interface

PCI/PXI	3.3 V or 5 V signal environment
DMA channels	6, can be used for analog input, digital input, digital output, counter/timer 0, counter/timer 1

The PXI device can be installed in PXI Express hybrid slots. It cannot be used to control SCXI in PXI/SCXI combo chassis.

## Power Requirements

Current draw from bus during no-load condition <sup>4</sup>	
+5 V	0.03 A
+3.3 V	0.725 A
+12 V	0.35 A
Current draw from bus during AI overvoltage condition <sup>4</sup>	
+5 V	0.03 A
+3.3 V	1.2 A
+12 V	0.38 A

<sup>3</sup> In other sections of this document, RTSI refers to RTSI <0..7> for the PCI devices or PXI\_TRIG <0..7> for PXI devices.

<sup>4</sup> Does not include P0/PFI/P1/P2 and +5 V terminals.

## Current Limits

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**Caution** Exceeding the current limits may cause unpredictable behavior by the device and/or PC/chassis.

PCI, +5 V terminal	1 A maximum <sup>5</sup>
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PXI

+5 V terminal	1 A maximum <sup>5</sup>
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P0/PFI/P1/P2 and +5 V terminals combined	2 A maximum
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## Physical Characteristics

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Dimensions

PCI printed circuit board	10.6 cm × 15.5 cm (4.2 in. × 6.1 in.)
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PXI printed circuit board	Standard 3U PXI
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Weight

PCI	142 g (5 oz)
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PXI	212 g (7.5 oz)
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I/O connector	1 68-pin VHDCI
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## Calibration

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Recommended warm-up time	15 minutes
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Calibration interval	2 years
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## Maximum Working Voltage

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*Maximum working voltage* refers to the signal voltage plus the common-mode voltage.

Channel-to-earth	11 V, Measurement Category I
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Measurement Category I is for measurements performed on circuits not directly connected to the electrical distribution system referred to as MAINS voltage. MAINS is a hazardous live electrical supply system that powers equipment. This category is for measurements of voltages from specially protected secondary circuits. Such voltage measurements include signal levels,

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<sup>5</sup> Older revisions have a self-resetting fuse that opens when current exceeds this specification. Newer revisions have a traditional fuse that opens when current exceeds this specification. This fuse is not customer-replaceable; if the fuse permanently opens, return the device to NI for repair.

special equipment, limited-energy parts of equipment, circuits powered by regulated low-voltage sources, and electronics.



**Caution** Do not use for measurements within Categories II, III, or IV.



**Note** Measurement Categories CAT I and CAT O (Other) are equivalent. These test and measurement circuits are not intended for direct connection to the MAINS building installations of Measurement Categories CAT II, CAT III, or CAT IV.

## Environmental

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Operating temperature	0 °C to 55 °C
Storage temperature	-20 °C to 70 °C
Humidity	10% RH to 90% RH, noncondensing
Maximum altitude	2,000 m
Pollution Degree (indoor use only)	2

Indoor use only.

## Shock and Vibration (PXI Only)

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Operational shock	30 g peak, half-sine, 11 ms pulse (Tested in accordance with IEC 60068-2-27. Test profile developed in accordance with MIL-PRF-28800F.)
Random vibration	
Operating	5 Hz to 500 Hz, 0.3 g <sub>rms</sub>
Nonoperating	5 Hz to 500 Hz, 2.4 g <sub>rms</sub> (Tested in accordance with IEC 60068-2-64. Nonoperating test profile exceeds the requirements of MIL-PRF-28800F, Class 3.)

## Safety

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This product is designed to meet the requirements of the following electrical equipment safety standards for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA 61010-1



**Note** For UL and other safety certifications, refer to the product label or the [Online Product Certification](#) section.

# Electromagnetic Compatibility

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This product meets the requirements of the following EMC standards for electrical equipment for measurement, control, and laboratory use:

- EN 61326-1 (IEC 61326-1): Class A emissions; Basic immunity
- EN 55011 (CISPR 11): Group 1, Class A emissions
- EN 55022 (CISPR 22): Class A emissions
- EN 55024 (CISPR 24): Immunity
- AS/NZS CISPR 11: Group 1, Class A emissions
- AS/NZS CISPR 22: Class A emissions
- FCC 47 CFR Part 15B: Class A emissions
- ICES-001: Class A emissions



**Note** In the United States (per FCC 47 CFR), Class A equipment is intended for use in commercial, light-industrial, and heavy-industrial locations. In Europe, Canada, Australia and New Zealand (per CISPR 11) Class A equipment is intended for use only in heavy-industrial locations.



**Note** Group 1 equipment (per CISPR 11) is any industrial, scientific, or medical equipment that does not intentionally generate radio frequency energy for the treatment of material or inspection/analysis purposes.



**Note** For EMC declarations and certifications, and additional information, refer to the [Online Product Certification](#) section.

## CE Compliance

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This product meets the essential requirements of applicable European Directives, as follows:

- 2014/35/EU; Low-Voltage Directive (safety)
- 2014/30/EU; Electromagnetic Compatibility Directive (EMC)
- 2011/65/EU; Restriction of Hazardous Substances (RoHS)

## Online Product Certification

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Refer to the product Declaration of Conformity (DoC) for additional regulatory compliance information. To obtain product certifications and the DoC for this product, visit [ni.com/certification](https://ni.com/certification), search by model number or product line, and click the appropriate link in the Certification column.

# Environmental Management

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NI is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial to the environment and to NI customers.

For additional environmental information, refer to the *Minimize Our Environmental Impact* web page at [ni.com/environment](https://ni.com/environment). This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

## Waste Electrical and Electronic Equipment (WEEE)



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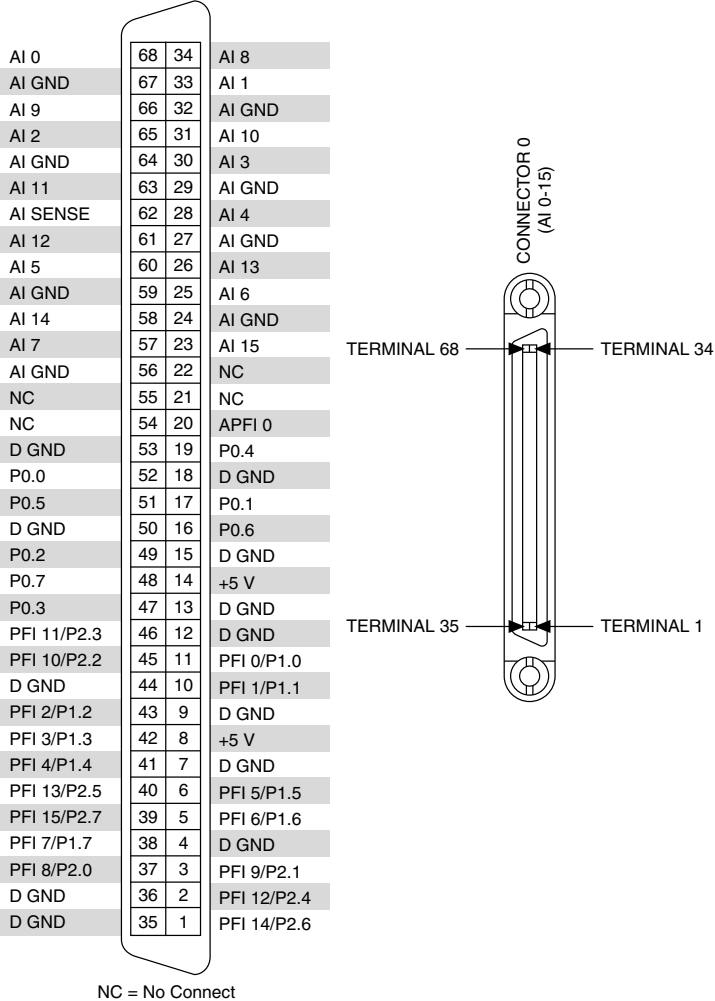


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# Device Pinout

**Figure 8. NI PCI/PXI-6250 Pinout**



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