PXI

PXI-1033 User Manual

October 2019
371991D-01

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While this hardware is compliant with the applicable regulatory EMC requirements, there is no guarantee that interference will not occur in a particular installation. To minimize the potential for the hardware to cause interference to radio and television reception or to experience unacceptable performance degradation, install and use this hardware in strict accordance with the instructions in the hardware documentation and the DoC[1].

If this hardware does cause interference with licensed radio communications services or other nearby electronics, which can be determined by turning the hardware off and on, you are encouraged to try to correct the interference by one or more of the following measures:

- Reorient the antenna of the receiver (the device suffering interference).
- Relocate the transmitter (the device generating interference) with respect to the receiver.
- Plug the transmitter into a different outlet so that the transmitter and the receiver are on different branch circuits.

Some hardware may require the use of a metal, shielded enclosure (windowless version) to meet the EMC requirements for special EMC environments such as, for marine use or in heavy industrial areas. Refer to the hardware's user documentation and the DoC[1] for product installation requirements.

When the hardware is connected to a test object or to test leads, the system may become more sensitive to disturbances or may cause interference in the local electromagnetic environment.

Operation of this hardware in a residential area is likely to cause harmful interference. Users are required to correct the interference at their own expense or cease operation of the hardware.

Changes or modifications not expressly approved by National Instruments could void the user's right to operate the hardware under the local regulatory rules.

[1] The Declaration of Conformity (DoC) contains important EMC compliance information and instructions for the user or installer. To obtain the DoC for this product, visit `ni.com/certification`, search by model number or product line, and click the appropriate link in the Certification column.

Contents

About This Manual

The *PXI-1033 User Manual* contains information about installing, configuring, using, and maintaining the PXI-1033 chassis.

Note For PXI-1033 specifications, refer to the *PXI-1033 Specifications* on ni.com.

ConventionsRelated Documentation

The following documents contain information that you might find helpful as you read this manual:

- *CompactPCI Specification PICMG 2.0 R 3.0*
- *PXI Hardware Specification*
- *PXI Software Specification*
- IEEE 1101.1-1991, *IEEE Standard for Mechanical Core Specifications for Microcomputers Using IEC 603-2 Connectors*
- IEEE 1101.10, *IEEE Standard for Additional Mechanical Specifications for Microcomputers Using IEEE 1101.1 Equipment Practice*

1 Getting Started

This chapter describes the key features of the PXI-1033 chassis and lists the kit contents and optional equipment you can order from National Instruments.

Unpacking

Carefully inspect the shipping container and the chassis for damage. Check for visible damage to the metal work. Check to make sure all handles, hardware, and switches are undamaged. Inspect the inner chassis for any possible damage, debris, or detached components. If damage appears to have been caused during shipment, file a claim with the carrier. Retain the packing material for possible inspection and/or reshipment.

What You Need to Get Started

- PXI-1033 chassis
- Filler panels
- AC power cable—refer to Table 1-1 for AC power cables
- *PXI-1033 User Manual*
- *Read Me First: Safety and Electromagnetic Compatibility*
- Driver CD-ROM containing NI PXI chassis software
- Chassis number labels
- MXI-Express x1 cable
- MXI-Express x1 host controller card

Table 1-1. AC Power Cables

Power Cable	Reference Standards
Standard 120 V (USA)	NEMA 5-15
Switzerland 220 V	SEV
Australia 240 V	AS C112
Universal Euro 230 V	CEE (7), II, IV, VII IEC83
North America 240 V	NEMA 6-15

Table 1-1. AC Power Cables (Continued)

Power Cable	Reference Standards
United Kingdom 230 V	BS 1363/IEC83
Japan 100 V	JIS C8303

If you are missing any of the items listed in Table 1-1, or if you have the incorrect AC power cable, contact National Instruments.

Key Features

The PXI-1033 combines a 5-slot PXI backplane with a structural design that has been optimized for maximum usability in a wide range of applications.

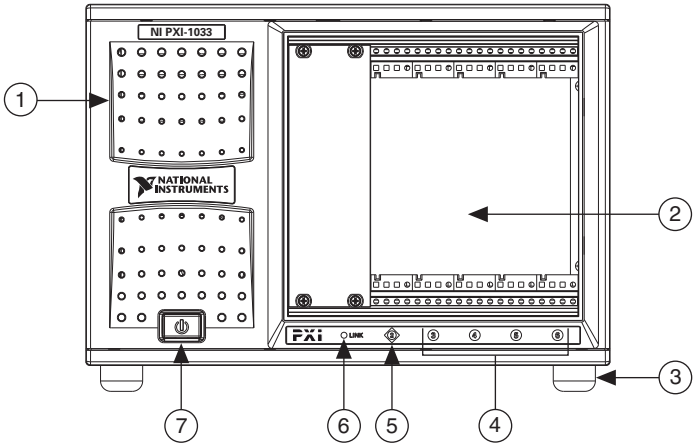
The key features of the chassis include the following:

- Accepts 3U PXI and CompactPCI (PICMG 2.0 R 3.0) modules
- 5 peripheral slots in a rugged, compact chassis with universal AC input, and automatic voltage/frequency ranging
- Integrated MXI-Express controller
- On/Off (Standby) power switch on the front panel for easy access
- AUTO/HIGH temperature-controlled fan speed based on air-intake temperature to minimize audible noise
- Carrying handle for portability (optional)
- Rack mountable

Chassis Description

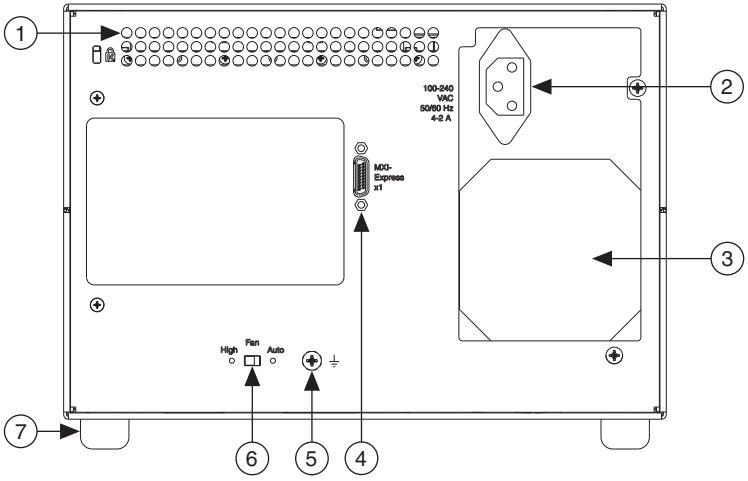
Figure 1-1 and Figure 1-2 show the key features of the PXI-1033 chassis front and rear panels. Figure 1-1 shows the front view of the PXI-1033. Figure 1-2 shows the rear view of the PXI-1033. Figure 1-3 shows the bottom of the chassis.

Figure 1-1. Front View of the PXI-1033 Chassis



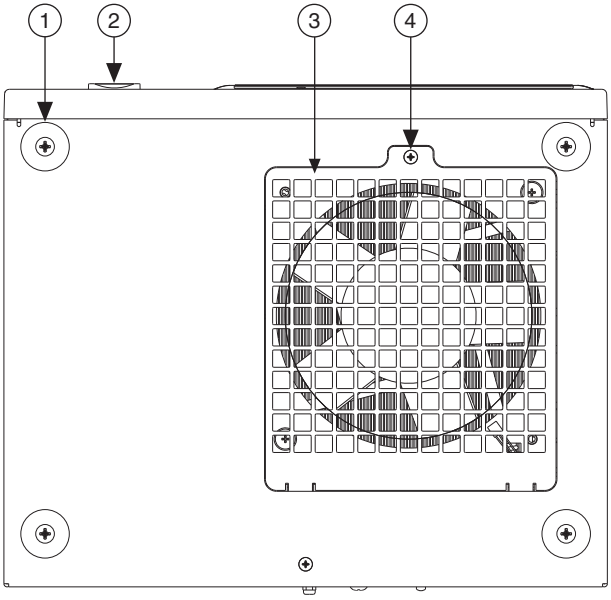
1 Power Supply Airflow Intake Vents
2 PXI Backplane
3 Rubber Foot
4 Generic Peripheral Slots
5 Star Trigger/Peripheral Slot
6 Link LED
7 Power Switch

Figure 1-2. Rear View of the PXI-1033 Chassis



1 Rear Exhaust Vents
2 AC Input
3 Power Supply Fan Exhaust
4 NI MXI-Express Chassis Controller Connector
5 Chassis Ground Screw
6 Auto/High Fan Speed Selector Switch
7 Rubber Foot

Figure 1-3. Bottom View of the PXI-1033 Chassis



1	Rubber Foot	3	Air Filter Cover
2	Power Switch	4	Air Filter Cover Screw

Optional Equipment

Contact National Instruments to order the following options for the PXI-1033 chassis.

EMC Filler Panels

Optional EMC filler panel kits are available from National Instruments.

Rack Mount Kit

A rack mount kit option is available for mounting the PXI-1033 chassis into a 19 in. instrument cabinet.

Handle/Feet Kit

An optional side handle and rubber feet kit is available from National Instruments to provide a handle for portability.

PXI-1033 Backplane Overview

Interoperability with PXI-1033 CompactPCI

The PXI-1033 backplane is interoperable with 5 V and universal PXI-compatible products and standard CompactPCI products. This is an important feature, because some PXI systems may require components that do not implement PXI-specific features. For example, you may want to use a standard CompactPCI network interface card in a PXI chassis.

The signals on the backplane P1 connectors meet the requirements of the CompactPCI specification for both peripheral and system modules. Refer to Appendix A, *Pinouts*, for pinout information.

The PXI-specific signals are on the backplane P2 connectors and are found only on those signal lines reserved or not used in the CompactPCI 64-bit specification. Therefore, all modules that meet the requirements of the CompactPCI 64-bit specification will function in the PXI-1033. Refer to Appendix A, *Pinouts*, for pinout information.

Note The PXI-1033 backplane is 32-bit PCI. 64-bit CompactPCI cards will operate in 32-bit mode in this chassis.

The chassis backplane has +5 V V(I/O). Refer to the *CompactPCI Specification PICMG 2.0 R 3.0* for details regarding V(I/O).

MXI Interface

The PXI-1033 chassis has a built-in x1 MXI-Express interface that can be accessed through the MXI-Express chassis controller connector on the back of the chassis, as shown in Figure 1-2.

The MXI interface can be cabled to a remote system with a host card using a x1 MXI-Express cable to provide control of the PXI-1033 chassis.

Star Trigger Slot

The star trigger (ST) slot is slot 2. This slot has dedicated equal-length trigger lines between slot 2 and peripheral slots 3 through 6 (refer to Figure 1-4). Slot 2 is intended for modules with ST functionality that can provide individual triggers to all other peripheral modules. However, if you do not require advanced trigger functionality, you can install any standard peripheral module in this slot.

The star trigger slot can also be used to provide a PXI_CLK10 signal to the backplane. For more information regarding PXI_CLK10, refer to the *System Reference Clock* section.

Peripheral Slots

There are five peripheral slots, including the star trigger slot.

Local Bus

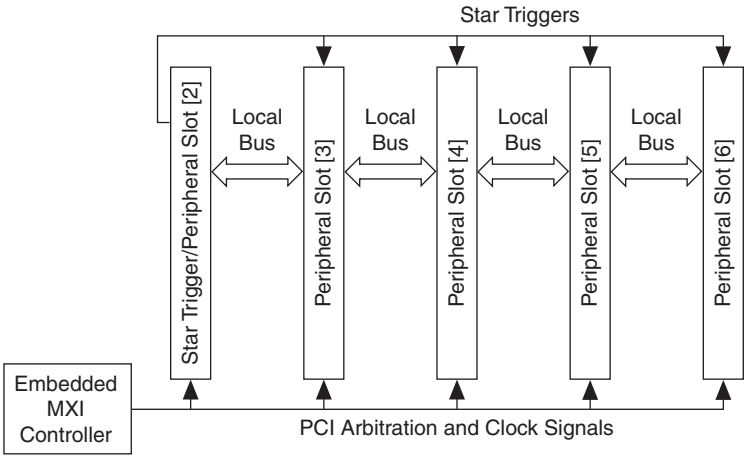
The PXI backplane local bus is a daisy-chained bus that connects each peripheral slot with adjacent peripheral slots to the left and right (refer to Figure 1-4). For example, the right local bus of slot 2 connects to the left local bus of slot 3, and so on.

The left local bus signal lines on slot 2 are used for star trigger. The right local bus signal lines on slot 6 are not routed anywhere.

Each local bus is 13 lines wide and can pass analog signals up to 42 V between cards or provide a high-speed TTL side-band digital communication path that does not reduce the PXI bus bandwidth.

Initialization software uses the configuration information specific to adjacent peripheral modules to evaluate local bus compatibility.

Figure 1-4. PXI Star Trigger and Local Bus Routing



Trigger Bus

All slots share eight PXI trigger lines. You can use these trigger lines in a variety of ways. For example, you can use triggers to synchronize the operation of several different PXI peripheral modules. In other applications, one module located in slot 2 can control carefully timed sequences of operations performed on other modules in the system. Modules can pass triggers to one another, allowing precisely timed responses to asynchronous external events the system is monitoring or controlling.

System Reference Clock

The PXI-1033 supplies the PXI 10 MHz system clock signal (PXI_CLK10) independently to each peripheral slot. An independent buffer (having a source impedance matched to the backplane and a skew of less than 250 ps between slots) drives the clock signal to each peripheral slot. You can use this common reference clock signal to synchronize multiple modules in a measurement or control system. You can drive PXI_CLK10 from an external source through the PXI_CLK10_IN pin on the P2 connector of the star trigger slot. Refer to Table A-2, *P2 (J2) Connector Pinout for the Star Trigger Slot*. You must manually switch S1 on the chassis backplane to enable or disable routing an external clock to peripheral slots.

2

Installation and Configuration

This chapter describes how to install, configure, and use the PXI-1033 chassis.

Before connecting the chassis to a power source, read this chapter and the *Read Me First: Safety and Electromagnetic Compatibility* document included with your chassis.

Safety Information

Caution Before undertaking any troubleshooting, maintenance, or exploratory procedure, carefully read the following caution notices.

This equipment contains voltage hazardous to human life and safety, and is capable of inflicting personal injury.

- **Chassis Grounding**—The chassis requires a connection from the premise wire safety ground to the chassis ground. The earth safety ground must be connected during use of this equipment to minimize shock hazards. Refer to the *Connecting Safety Ground* section for instructions on connecting safety ground.
- **Live Circuits**—Operating personnel and service personnel *must not* remove protective covers when operating or servicing the chassis. Adjustments and service to internal components must be undertaken by qualified service technicians. During service of this product, the mains connector to the premise wiring must be disconnected. Dangerous voltages may be present under certain conditions; use extreme caution.
- **Explosive Atmosphere**—Do *not* operate the chassis in conditions where flammable gases are present. Under such conditions, this equipment is unsafe and may ignite the gases or gas fumes.
- **Part Replacement**—Only service this equipment with parts that are exact replacements, both electrically and mechanically. Contact National Instruments for replacement part information. Installation of parts with those that are not direct replacements may cause harm to personnel operating the chassis. Furthermore, damage or fire may occur if replacement parts are unsuitable.
- **Modification**—Do *not* modify any part of the chassis from its original condition. Unsuitable modifications may result in safety hazards.

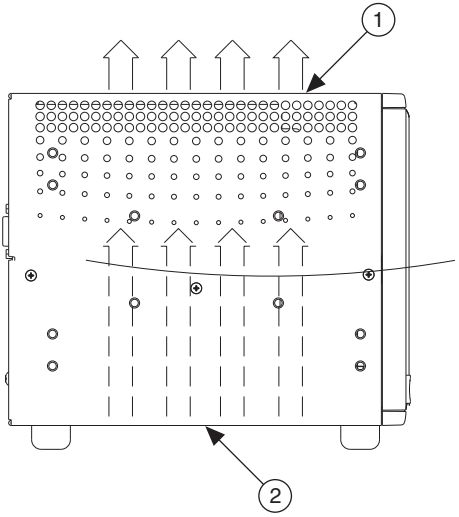
Chassis Cooling Considerations

The chassis is designed to operate on a bench or in an instrument rack. Determine how you want to use the chassis and follow the appropriate installation instructions.

Providing Adequate Clearance

Apertures in the top, bottom, front, rear, and along the right side of the chassis facilitate power supply and module cooling. Air enters through filters and fan inlet in the bottom of the chassis for module cooling. It then exits through the upper sections at the right side, back, and through the top, as shown in Figure 2-1. Air cooling the power supply enters the front of the chassis, which is shown in Figure 1-1, *Front View of the PXI-1033 Chassis*, then exits through the rear of the chassis, which is shown in Figure 1-2, *Rear View of the PXI-1033 Chassis*. Place the chassis on a bench top or in an instrument rack so that the fans (air inlets) and the air outlet apertures along the right side, the top, and the back of the chassis have adequate ventilation. Provide at least 44.5 mm (1.75 in.) clearance above, behind, and on the sides of the unit for adequate venting. High-power applications may require additional clearance.

Figure 2-1. PXI-1033 Module Cooling Airflow Side View



1	Air Outlets	2	Air Intake

Install the chassis so that you can easily access the bottom panel. This simplifies replacing the air filters, if necessary.

Setting Fan Speed

The AUTO/HIGH fan-speed selector switch is on the rear panel of the PXI-1033. Refer to Figure 1-2, *Rear View of the PXI-1033 Chassis*, to locate the fan-speed selector switch. Select HIGH for maximum cooling performance (recommended) or AUTO for quieter operation. The fan speed is determined by chassis intake air temperature when set to AUTO.

Installing Filler Panels

To improve module cooling performance, install filler panels (provided with the chassis) in unused or empty slots. Secure with the captive mounting screws provided.

Rack Mounting

Rack mount applications require the optional rack mount kits available from National Instruments. Refer to the *PXI-1033 Specifications* on `ni.com` and the instructions supplied with the rack mount kits to install your chassis in an instrument rack.

Note You may want to remove the feet from the chassis when rack mounting. To do so, remove the screws holding the feet in place.

Connecting Safety Ground

Caution The PXI-1033 chassis is designed with a three-position inlet that connects the cord set ground line to the chassis ground. To minimize shock hazard, make sure the electrical power outlet you use to power the chassis has an appropriate earth safety ground.

Connecting to Power Source

Cautions Do *not* install modules prior to performing the following power-on test.

To completely remove power, you *must* disconnect the power cable.

Attach input power through the rear AC inlet using the appropriate AC power cable supplied. Refer to Figure 1-2, *Rear View of the PXI-1033 Chassis*, to locate the AC inlet.

The power switch allows you to power on the chassis or place it in standby mode. Press the power switch to the On position (if not already on). Observe that all fans become operational.

Getting Started With The System

To set up and use your MXI-Express cards, you need the following:

- One NI MXI-Express x1 host card (PCI Express or ExpressCard) and one PXI-1033
- A MXI-Express x1 cable
- A host PC with an available x1 (or wider) PCI Express slot or a laptop with an available ExpressCard slot

Unpacking The MXI-Express Host Card

Your MXI-Express card is shipped in antistatic packages to prevent electrostatic damage (ESD) to the devices. ESD can damage several components on the device.

Caution *Never* touch the exposed pins of connectors. Doing so may damage the device.

To avoid such damage in handling the device, take the following precautions:

- Ground yourself using a grounding strap or by holding a grounded object.
- Touch the antistatic package to a metal part of the computer chassis before removing the device from the package.

Remove the device from the package and inspect the device for loose components or any sign of damage. Notify NI if the device appears damaged in any way. Do *not* install a damaged device into the computer or PXI/CompactPCI chassis.

Store the device in the antistatic envelope when not in use.

Hardware Installation

The following are general instructions for installing the MXI-Express card. Consult your computer user manual or technical reference manual for specific instructions and warnings.

Note The software for your MXI-Express kit is included with the current NI driver CD, and is installed with the PXI platform software included as part of NI-VISA and other NI driver software products.

Installing an NI ExpressCard Module

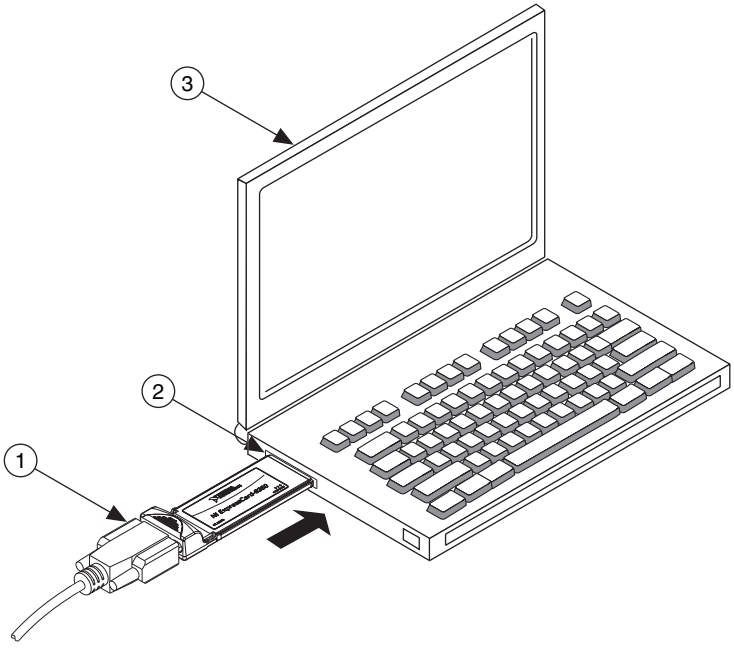
Because a laptop is not grounded, follow this procedure to safely connect it to your PXI system:

1. Install the NI ExpressCard module as directed in the previous section.
2. Touch the NI ExpressCard module and a metal part of the PXI chassis simultaneously.
3. Connect the cable to the NI ExpressCard module and the PXI-1033 chassis.
4. Plug the NI ExpressCard module into an available ExpressCard slot.

If your computer is already running (or hibernating) when you install NI ExpressCard module, you must reboot to detect the PXI system. Otherwise, the PXI system is detected when you start your computer.

Figure 2-5 shows how to insert the NI ExpressCard module and connect the cable.

Figure 2-2. Installing the NI ExpressCard Module



1	MXI-Express Cable	2	ExpressCard Slot	3	Portable Computer

Installing an NI PCI Express Host Card

The following steps detail the process of installing an NI PCI Express host card. Refer to Figure 2-3 to assist you in completing this procedure.

1. Power off your computer, but leave it plugged in while installing the NI PCI Express host card. The power cord grounds the chassis and protects it from electrical damage while you install the card.

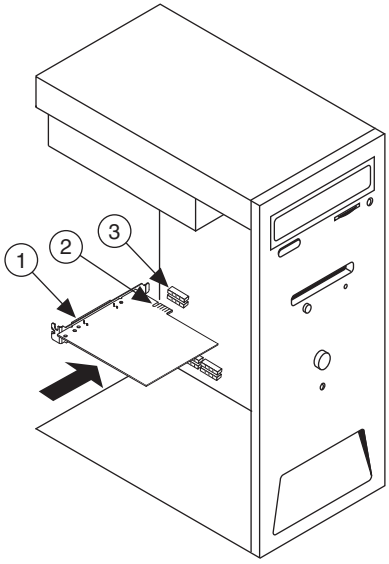
Caution To protect both yourself and the computer from electrical hazards, your computer should remain off until you finish installing the NI PCI Express host card.

2. Remove the top cover or access port to the PCI Express bus.
3. Select any available PCI Express expansion slot (x1 or wider).

Note The BIOS or motherboard may not support the NI PCI Express host card in a slot intended for a graphics card.

4. Locate the metal bracket that covers the cut-out in the back panel of the computer for the slot you have selected. Remove and save the bracket-retaining screw and the bracket cover.
5. Touch the metal part of the power supply case inside the computer to discharge any static electricity that might be on your clothes or body.
6. Line up the NI PCI Express host card with the slot on the back panel. Slowly push down on the top of the NI PCI Express host card until its card-edge connector is resting on the expansion slot receptacle. Using slow, evenly distributed pressure, press the NI PCI Express host card straight down until it seats in the expansion slot.
7. Reinstall the bracket-retaining screw to secure the NI PCI Express host card to the back panel rail.
8. Replace the computer cover.

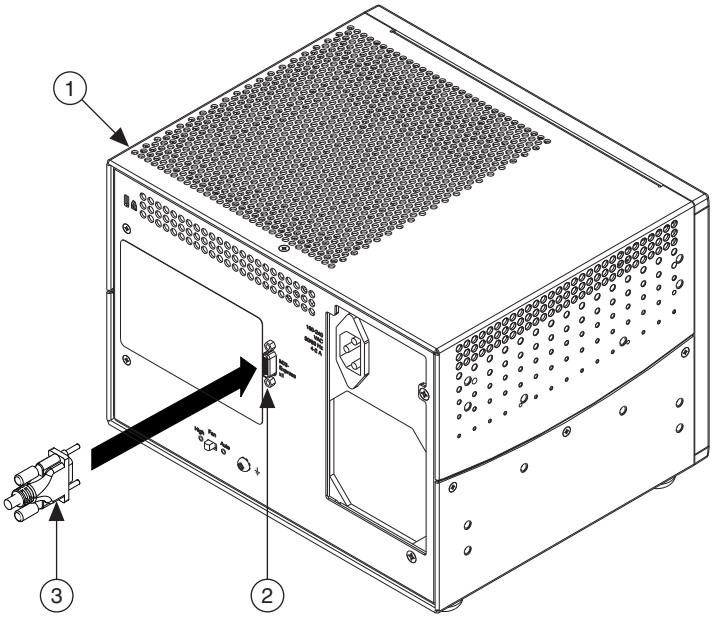
Figure 2-3. Installing the NI PCI Express Host Card



1	NI PCI Express Host Card	3	PCI Express Slot
2	PCI Express x1 Card-Edge Connector		

Figure 2-4 shows the cable connection with which a PCI Express host card controls the PXI-1033 chassis.

Figure 2-4. NI PCI Express Host Card Cable Connection



1 PXI-1033 Chassis
2 NI MXI-Express Chassis Controller Connector
3 MXI-Express Cable Connector

Cabling

1. Connect the appropriate MXI-Express cable to the NI PCI Express card and PXI-1033 chassis. The cables have no polarity, so either end may be connected to either connector.

Caution Do *not* remove the cable after the system is powered on. Doing so can hang or cause errors in applications communicating with devices behind MXI-Express. If a cable becomes unplugged, plug it back into the system.

Note For more information about cables, refer to the *MXI-Express Cable Options* section of this chapter.

Powering Up the MXI-Express System

1. Power on the chassis.
2. Power on the host.

Typical PCI-PCI bridges are used to add PCI devices to a PCI hierarchy in which all the bridges and devices are contained within a single chassis. Because of this, BIOSes and operating systems make the assumption that all PCI devices in the entire hierarchy will be available as soon as code execution begins at power-up time. This assumption means that all of the expansion chassis must be turned on before the host PC for the BIOS and OS to correctly configure a MXI-Express system.

Notes You may add additional chassis using MXI-3 or MXI-4. When using MXI-4, there are no restrictions on chassis power-on order, except that the host must be powered on last. When using MXI-3, you must power up the chassis in order, beginning furthest from the host and ending with the host. Refer to the MXI-3 or MXI-4 documentation for further details.

The PXI-1033 chassis will assert a wake signal on power up. The host PC also will power on if it supports this functionality.

Powering Down the MXI-Express System

Because operating systems and drivers commonly make the assumption that PCI devices will be present in the system from power-up to power-down, it is important not to power off the expansion chassis until after the host PC is powered off. Powering off the expansion chassis while the host is still on can cause crashes or hangs. The order in which expansion chassis are powered off, relative to each other, is not important.

Checking LEDs for Status

After powering on the PXI-1033 chassis you should check the LEDs for status to ensure that all connected systems have linked. The following table defines the LED states.

Table 2-1. LED Status for the PXI-1033 Chassis

LED	Color	Meaning
PWR	Off	No power
	Green	Power is within spec
	Red	Power is out of spec
LINK	Off	Link not established
	Green	Link established
	Alternating Green and Red	Link established and activity present on the PCI bus
	Red	Link corrupted due to MXI sideband signals
	Blinking Red	Link corrupted due to missing PCI Express Reference Clock

Functional Overview

MXI-Express is based on PCI Express technology. A MXI-Express kit uses a PCI Express switch and PCI Express-to-PCI bridge to enable control of a PXI or CompactPCI chassis from a PC with an available PCI Express slot. The PCI Express-to-PCI bridge architecture is transparent to device drivers, so no additional software is needed to support using PXI and CompactPCI devices in a chassis connected using MXI-Express.

The link between the PC and the chassis is a x1 cabled PCI Express link. This link is a dual-simplex communication channel comprised of a low-voltage, differentially driven signal pair. The link can transmit at a rate of 2.5 Gbps in each direction simultaneously.

MXI-Express Cable Options

MXI-Express is available with cables of various lengths. Table 2-2 shows the cables available from National Instruments.

Table 2-2. National Instruments MXI-Express Cables

Cable Length (Meters)	Description
1 m	MXI-Express copper cable
3 m	MXI-Express copper cable
7 m	MXI-Express copper cable

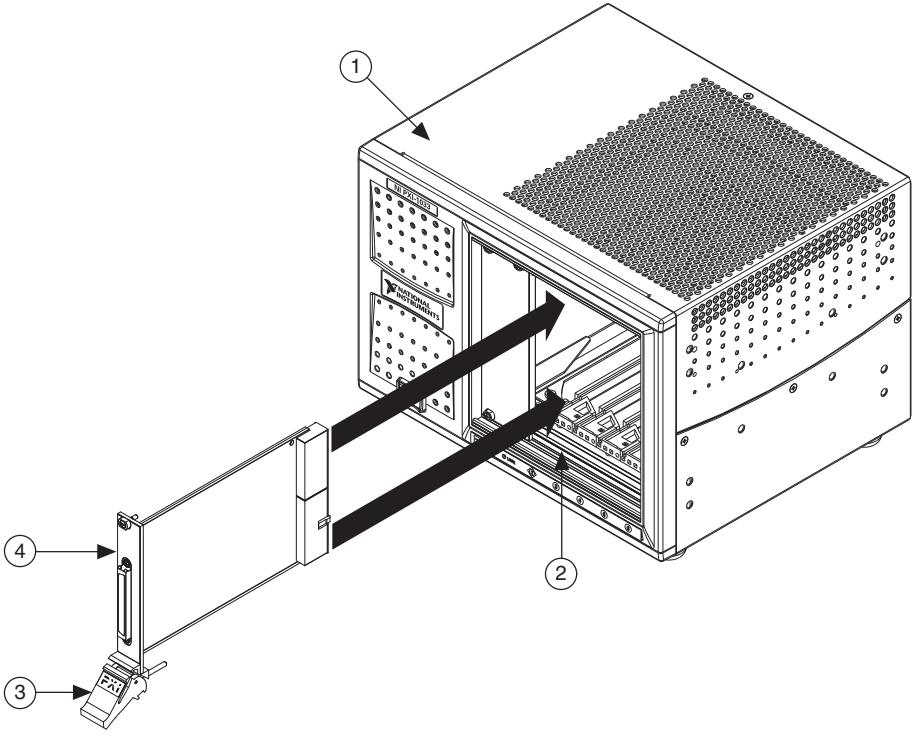
Installing PXI Modules

Complete the following steps to install a module.

1. Make sure the power switch is in the Off (Standby) position.
2. Install a module into a chassis slot by first placing the module card edges into the front module guides (top and bottom), as shown in Figure 2-5. Slide the module to the rear of the chassis, making sure that the injector/ejector handle is pushed down as shown in Figure 2-5.
3. When you begin to feel resistance, push up on the injector/ejector handle to fully inject the module into the chassis frame. Secure the module front panel to the chassis using the module front-panel mounting screws.

Note You must install the driver software provided on the NI Driver CD supplied with your kit before you can use the modules in the chassis.

Figure 2-5. Installing PXI or CompactPCI Modules

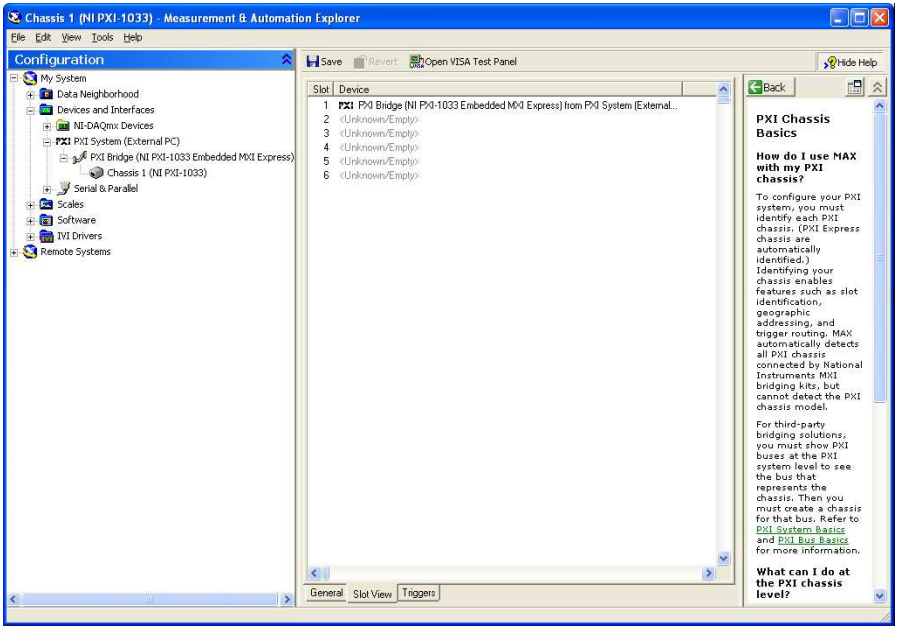


1	PXI-1033 Chassis	3	Injector/Ejector Handle
2	Injector/Ejector Rail	4	PXI Module

PXI System Configuration with MAX

Configuration of the PXI system is handled through Measurement & Automation Explorer (MAX), included on the NI Driver CD-ROM packaged with your kit. MAX creates the `pxisys.ini` file that defines the layout and parameters of your PXI system. After installing the software on the NI Driver CD-ROM, the MAX icon will be present on the desktop. The configuration steps for single or multiple chassis systems are the same.

Figure 2-6. Chassis Configuration in MAX



Basic PXI System Configuration

The *Platform Services* 2.1 (or higher) software provided on the NI Driver CD-ROM will automatically detect your PXI-1033 chassis. To manually configure your chassis follow the steps outlined below. Refer to Figure 2-6 while completing the following steps:

1. Launch MAX.
2. In the **Configuration** tree, click the **Devices and Interfaces** branch to expand it.
3. If the PXI system controller has not yet been configured, it will be labeled `PXI System (Unidentified)`. Right-click this entry to display the context menu and then select the appropriate controller model from the **Identify As** submenu.

4. Click the **PXI System** controller, and the chassis (or multiple chassis, in a multi-chassis configuration) will be listed below it. Identify each chassis by right-clicking its entry and then selecting **External PC** through the **Identify As** submenu. Further expanding the **PXI System** branch will show all of the devices in the system that can be recognized by NI-VISA. After your controller and all of your chassis have been identified, the required `pxisys.ini` file will be complete.
5. Apply the chassis number labels (shown in Figure 2-7) included with your kit to each chassis in your PXI system, and write the chassis number accordingly in the white space.

Figure 2-7. Chassis Number Label

Trigger Configuration in MAX

Each chassis has one or more trigger buses, each with eight lines numbered 0 through 7 that can be reserved and routed statically or dynamically. Static reservation “pre-allocates” a trigger line to prevent its configuration by a user program. Dynamic reservation/routing/deallocation is performed *on the fly* within a user program based upon National Instruments APIs such as NI-DAQmx. Static reservation of trigger lines can be implemented by the user in MAX through the **Triggers** tab. Reserved trigger lines will not be used by PXI modules dynamically configured by programs such as NI-DAQmx. This prevents the instruments from double-driving the trigger lines, possibly damaging devices in the chassis.

Complete the following steps to reserve these trigger lines in MAX.

1. In the **Configuration** tree, click the PXI chassis branch you want to configure.
2. Click the **Triggers** tab at the bottom of the right-hand pane.
3. Select which trigger lines you want to statically reserve.
4. Click the **Apply** button.

Note For more information about routing and reserving PXI triggers refer to KnowledgeBase **3TJDOND8** at `ni.com/support`.

Using System Configuration and Initialization Files

The PXI specification allows many combinations of PXI chassis and system modules. To assist system integrators, the manufacturers of PXI chassis and system modules must document the capabilities of their products. The minimum documentation requirements are contained in `.ini` files, which consist of ASCII text. System integrators, configuration utilities, and device drivers can use these `.ini` files.

The capability documentation for the PXI-1033 chassis is contained in the `chassis.ini` file on the software media that comes with the chassis. The information in this file is combined with information about the system controller to create a single system initialization file called `pxisys.ini` (PXI System Initialization). The system controller manufacturer either provides a `pxisys.ini` file for the particular chassis model that contains the system controller or provides a utility that can read an arbitrary `chassis.ini` file and generate the corresponding `pxisys.ini` file. System controllers from National Instruments use MAX to generate the `pxisys.ini` file from the `chassis.ini` file.

Device drivers and other utility software read the `pxisys.ini` file to obtain system information. Device drivers should have no need to directly read the `chassis.ini` file. For detailed information regarding initialization files, refer to the PXI specification at `www.pxisa.org`.

3

Maintenance

This chapter describes basic maintenance procedures you can perform on the PXI-1033 chassis.

Caution Disconnect the power cables prior to servicing the chassis.

Service Interval

Clean the chassis fan filter at a maximum interval of six months. Depending on the amount of use and ambient dust levels in the operating environment, the filter may require more frequent cleaning.

Clean dust from the chassis exterior (and interior) as needed, based on the operating environment. Periodic cleaning increases reliability.

Preparation

The information in this chapter is designed for use by qualified service personnel. Read the *Read Me First: Safety and Radio-Frequency Interference* document included with your kit before attempting any procedures in this chapter.

Caution Many components within the chassis are susceptible to static discharge damage. Service the chassis only in a static-free environment. Observe standard handling precautions for static-sensitive devices while servicing the chassis. Always wear a grounded wrist strap or equivalent while servicing the chassis.

Cleaning

Caution *Always* disconnect the AC power cable before cleaning or servicing the chassis.

Cleaning procedures consist of exterior and interior cleaning of the chassis and cleaning the fan filter. Refer to your module user documentation for information on cleaning individual CompactPCI or PXI modules.

Interior Cleaning

Use a dry, low-velocity stream of air to clean the interior of the chassis. Use a soft-bristle brush for cleaning around components.

Exterior Cleaning

Cautions Avoid getting moisture inside the chassis during exterior cleaning, especially through the top vents. Use just enough moisture to dampen the cloth.

Do *not* wash the front- or rear-panel connectors or switches. Cover these components while cleaning the chassis.

Do *not* use harsh chemical cleaning agents; they may damage the chassis. Avoid chemicals that contain benzene, toluene, xylene, acetone, or similar solvents.

Clean the exterior surfaces of the chassis with a dry lint-free cloth or a soft-bristle brush. If any dirt remains, wipe with a cloth moistened in a mild soap solution. Remove any soap residue by wiping with a cloth moistened with clear water. Do *not* use abrasive compounds on any part of the chassis.

Cleaning and Replacing the Fan Filter

A dirty fan filter can dramatically reduce the cooling performance of the chassis. Clean the filter whenever it becomes visibly dirty. You can easily remove the chassis air filter from the bottom of the chassis by removing the filter cover. To remove the filter cover, loosen the retainer screw. The filter cover is shown in Figure 1-3, *Bottom View of the PXI-1033 Chassis*.

Clean the fan filter by washing it in a mild soap solution and then vacuuming or blowing air through it. Rinse the filter with water and allow it to dry before reinstalling it on the chassis.

You can replace the fan filter with part number 150139-C from Air Filtration Products, Inc., Tucson, AZ 85705, if necessary.

A

Pinouts

This appendix describes the P1 and P2 connector pinouts for the PXI-1033 backplane.

Table A-1 shows the P1 (J1) Connector Pinout for the star trigger slot.

Table A-2 shows the P2 (J2) Connector Pinout for the star trigger slot.

Table A-3 shows the P1 (J1) Connector Pinout for the peripheral slots.

Table A-4 shows the P2 (J2) Connector Pinout for the peripheral slots.

Note PXI signals are shown in bold.

Table A-1. P1 (J1) Connector Pinout for the Star Trigger Slot

Pin	Z	A	B	C	D	E	F
25	GND	5V	REQ64#	ENUM#	3.3V	5V	GND
24	GND	AD[1]	5V	V(I/O)	AD[0]	ACK64#	GND
23	GND	3.3V	AD[4]	AD[3]	5V	AD[2]	GND
22	GND	AD[7]	GND	3.3V	AD[6]	AD[5]	GND
21	GND	3.3V	AD[9]	AD[8]	M66EN	C/BE[0]#	GND
20	GND	AD[12]	GND	V(I/O)	AD[11]	AD[10]	GND
19	GND	3.3V	AD[15]	AD[14]	GND	AD[13]	GND
18	GND	SERR#	GND	3.3V	PAR	C/BE[1]#	GND
17	GND	3.3V	IPMB_SCL	IPMB_SDA	GND	PERR#	GND
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND
15	GND	3.3V	FRAME#	IRDY#	BD_SEL#	TRDY#	GND
12–14	Key Area						
11	GND	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	GND
10	GND	AD[21]	GND	3.3V	AD[20]	AD[19]	GND
9	GND	C/BE[3]#	IDSEL	AD[23]	GND	AD[22]	GND
8	GND	AD[26]	GND	V(I/O)	AD[25]	AD[24]	GND
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]	GND
6	GND	REQ#	GND	3.3V	CLK	AD[31]	GND
5	GND	BRSVP1A5	BRSVP1B5	RST#	GND	GNT#	GND
4	GND	IPMB_PWR	HEALTHY#	V(I/O)	INTP	INTS	GND
3	GND	INTA#	INTB#	INTC#	5V	INTD#	GND
2	GND	TCK	5V	TMS	TDO	TDI	GND
1	GND	5V	–12V	TRST#	+12V	5V	GND

Table A-2. P2 (J2) Connector Pinout for the Star Trigger Slot

Pin	Z	A	B	C	D	E	F
22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND	**PXI_LBR0**	RSV	**PXI_LBR1**	**PXI_LBR2**	**PXI_LBR3**	GND
20	GND	**PXI_LBR4**	**PXI_LBR5**	**PXI_STAR0**	GND	**PXI_STAR1**	GND
19	GND	**PXI_STAR2**	RSV	**PXI_STAR3**	**PXI_STAR4**	**PXI_STAR5**	GND
18	GND	**PXI_TRIG3**	**PXI_TRIG4**	**PXI_TRIG5**	GND	**PXI_TRIG6**	GND
17	GND	**PXI_TRIG2**	GND	RSV	**PXI_CLK10_IN**	**PXI_CLK10**	GND
16	GND	**PXI_TRIG1**	**PXI_TRIG0**	RSV	GND	**PXI_TRIG7**	GND
15	GND	**PXI_BRSVA15**	GND	RSV	**PXI_STAR6**	**PXI_LBR6**	GND
14	GND	RSV	RSV	RSV	GND	RSV	GND
13	GND	RSV	GND	V(I/O)	RSV	RSV	GND
12	GND	RSV	RSV	RSV	GND	RSV	GND
11	GND	RSV	GND	V(I/O)	RSV	RSV	GND
10	GND	RSV	RSV	RSV	GND	RSV	GND
9	GND	RSV	GND	V(I/O)	RSV	RSV	GND
8	GND	RSV	RSV	RSV	GND	RSV	GND
7	GND	RSV	GND	V(I/O)	RSV	RSV	GND
6	GND	RSV	RSV	RSV	GND	RSV	GND
5	GND	RSV	GND	V(I/O)	RSV	RSV	GND
4	GND	V(I/O)	**PXI_BRSVB4**	RSV	GND	RSV	GND
3	GND	**PXI_LBR7**	GND	**PXI_LBR8**	**PXI_LBR9**	**PXI_LBR10**	GND
2	GND	**PXI_LBR11**	**PXI_LBR12**	UNC	**PXI_STAR7**	**PXI_STAR8**	GND
1	GND	**PXI_STAR9**	GND	**PXI_STAR10**	**PXI_STAR11**	**PXI_STAR12**	GND

Table A-3. P1 (J1) Connector Pinout for the Generic Peripheral Slot

Pin	Z	A	B	C	D	E	F
25	GND	5V	REQ64#	ENUM#	3.3V	5V	GND
24	GND	AD[1]	5V	V(I/O)	AD[0]	ACK64#	GND
23	GND	3.3V	AD[4]	AD[3]	5V	AD[2]	GND
22	GND	AD[7]	GND	3.3V	AD[6]	AD[5]	GND
21	GND	3.3V	AD[9]	AD[8]	M66EN	C/BE[0]#	GND
20	GND	AD[12]	GND	V(I/O)	AD[11]	AD[10]	GND
19	GND	3.3V	AD[15]	AD[14]	GND	AD[13]	GND
18	GND	SERR#	GND	3.3V	PAR	C/BE[1]#	GND
17	GND	3.3V	IPMB_SCL	IPMB_SDA	GND	PERR#	GND
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND
15	GND	3.3V	FRAME#	IRDY#	BD_SEL#	TRDY#	GND
12–14	Key Area						
11	GND	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	GND
10	GND	AD[21]	GND	3.3V	AD[20]	AD[19]	GND
9	GND	C/BE[3]#	IDSEL	AD[23]	GND	AD[22]	GND
8	GND	AD[26]	GND	V(I/O)	AD[25]	AD[24]	GND
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]	GND
6	GND	REQ#	GND	3.3V	CLK	AD[31]	GND
5	GND	BRSVP1A5	BRSVP1B5	RST#	GND	GNT#	GND
4	GND	IPMB_PWR	HEALTHY#	V(I/O)	INTP	INTS	GND
3	GND	INTA#	INTB#	INTC#	5V	INTD#	GND
2	GND	TCK	5V	TMS	TDO	TDI	GND
1	GND	5V	–12V	TRST#	+12V	5V	GND

Table A-4. P2 (J2) Connector Pinout for the Generic Peripheral Slot

Pin	Z	A	B	C	D	E	F
22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND	**PXI_LBR0**	RSV	**PXI_LBR1**	**PXI_LBR2**	**PXI_LBR3**	GND
20	GND	**PXI_LBR4**	**PXI_LBR5**	**PXI_LBL0**	GND	**PXI_LBL1**	GND
19	GND	**PXI_LBL2**	RSV	**PXI_LBL3**	**PXI_LBL4**	**PXI_LBL5**	GND
18	GND	**PXI_TRIG3**	**PXI_TRIG4**	**PXI_TRIG5**	GND	**PXI_TRIG6**	GND
17	GND	**PXI_TRIG2**	GND	RSV	**PXI_STAR**	**PXI_CLK10**	GND
16	GND	**PXI_TRIG1**	**PXI_TRIG0**	RSV	GND	**PXI_TRIG7**	GND
15	GND	**PXI_BRSVA15**	GND	RSV	**PXI_LBL6**	**PXI_LBR6**	GND
14	GND	RSV	RSV	RSV	GND	RSV	GND
13	GND	RSV	GND	V(I/O)	RSV	RSV	GND
12	GND	RSV	RSV	RSV	GND	RSV	GND
11	GND	RSV	GND	V(I/O)	RSV	RSV	GND
10	GND	RSV	RSV	RSV	GND	RSV	GND
9	GND	RSV	GND	V(I/O)	RSV	RSV	GND
8	GND	RSV	RSV	RSV	GND	RSV	GND
7	GND	RSV	GND	V(I/O)	RSV	RSV	GND
6	GND	RSV	RSV	RSV	GND	RSV	GND
5	GND	RSV	GND	V(I/O)	RSV	RSV	GND
4	GND	V(I/O)	64EN#	RSV	GND	RSV	GND
3	GND	**PXI_LBR7**	GND	**PXI_LBR8**	**PXI_LBR9**	**PXI_LBR10**	GND
2	GND	**PXI_LBR11**	**PXI_LBR12**	UNC	**PXI_LBL7**	**PXI_LBL8**	GND
1	GND	**PXI_LBL9**	GND	**PXI_LBL10**	**PXI_LBL11**	**PXI_LBL12**	GND

B

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Index

L

M

N

O

P

R

S

T

U