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NI-9269

Board Assembly Part Number(s)

Part Number	Description
198920A-01L	4-CH ISOLATED ANALOG OUTPUT
198920A-03L	4-CH ISOLATED ANALOG OUTPUT, CONFORMAL COATED

Manufacturer: National Instruments

Volatile Memory

Type	Size	User Accessible/ System Accessible[1]	Battery Backup?	Purpose	Method of Clearing[2]
DAC	(x4)2 bytes	Yes/Yes	No	Stores last output value	Cycle power
CPLD RAM	(x4) 2 bytes	No/Yes	No	Stores last output value	Cycle power
CPLD RAM	3 bytes	No/Yes	No	Stores channel status	Cycle power

Non-Volatile Memory

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
EEPROM	1 KB	No/Yes	No	Module ID and calibration	None available to user
CPLD	192 Macrocell	No/No	No	Module operation	None available to user
Digital Potentiometer	8-bit	No/No	No	Isolation common mode compensation	None available to user

Media Storage

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing

NONE

Clearing Notes:

EEPROM: User accessibility of the calibration EEPROM is exposed through an external calibration Applications Programming Interface (API) in LabVIEW. To declassify this memory, complete the steps listed in KB 4GHLANQE (Clearing the User-Accessible EEPROM on an NI-DAQmx Supported Device).

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[1] Items are designated **No** for the following reason(s):
a) Hardware changes or a unique software tool from National Instruments are required to modify contents of the memory listed.
b) Hardware-modifying software tools are not distributed to customers for any personal access or customization, also known as non-normal use.

[2] The designation *None Available to User* indicates that the ability to clear this memory is not available to the user under normal operation. The utilities required to clear the memory are not distributed by National Instruments to customers for normal use.

Terms and Definitions

User Accessible Allows the user to directly write or modify the contents of the memory during normal instrument operation.

System Accessible Does not allow the user to access or modify the memory during normal instrument operation.   However, system accessible memory may be accessed or modified by background processes. This can be something that is not deliberate by the user and can be a background driver implementation, such as storing application information in RAM to increase speed of use.

Cycle Power The process of completely removing power from the device and its components. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory Requires power to maintain the stored information. When power is removed from this memory, its contents are lost.

Non-Volatile Retains its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.