Board Model Names
9623/26/33/36 Series (sbRIO-9623, sbRIO-9626, sbRIO-9633, sbRIO-9636)

Board Part Number Range
000000-00 (Please Call 1-866-ASK-MYNI for assistance with PN)

Manufacturer: National Instruments

Volatile Memory

Type[1]	Size	User Accessible/ System Accessible[2]	Battery Backup?	Purpose	Method of Clearing[3]
(9623)					
DRAM	128 MB	Yes/Yes	No	RAM	Cycle power
(9626)					
DRAM	256 MB	Yes/Yes	No	RAM	Cycle power
(9633)					
DRAM	128 MB	Yes/Yes	No	RAM	Cycle power
(9636)					
DRAM	256 MB	Yes/Yes	No	RAM	Cycle power
(9623)					
FPGA	Spartan-6 LX25	Yes/Yes	No	FPGA	Cycle power
(9626)					
FPGA	Spartan-6 LX45	Yes/Yes	No	FPGA	Cycle power
(9633)					
FPGA	Spartan-6 LX25	Yes/Yes	No	FPGA	Cycle power
(9636)					
FPGA	Spartan-6 LX45	Yes/Yes	No	FPGA	Cycle power

Non-Volatile Memory

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
CPLD	2 Kbits	No/No	No	General Logic	None available to user
(9623)					
SSD	256 MB	Yes/Yes	No	Primary Storage	Reformatting the drive
-BSP Firmware		No/No	No	Firmware BSP	None available to user
-FPGA Config		Yes/No	No	FPGA Startup	Clear Startup App

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[1] Calibration constants that are stored in device EEPROMs include information for the device's full operating range. Calibration constants do not maintain any unique data for specific configurations at which the device is used unless otherwise specified.

[2] Items are designated **No** for the following reason(s): a) Hardware changes or a unique software tool from National Instruments are required to modify contents of the memory listed. b) Hardware modifying software tools are not distributed to public users for any personal access or customization; also known as non-normal use.

[3] The designation *None Available to User* indicates that the ability to clear this memory is not available to the user under normal operation. The utilities required to clear the memory are not distributed by National Instruments to customers for normal use.

(9626)					
SSD	*512 MB*	*Yes/Yes*	*No*	*Primary Storage*	*Reformatting the drive*
-BSP Firmware		*No/No*	*No*	*Firmware BSP*	*None available to user*
-FPGA Config		*Yes/No*	*No*	*FPGA Startup*	*Clear Startup App*
(9633)					
SSD	*256 MB*	*Yes/Yes*	*No*	*Primary Storage*	*Reformatting the drive*
-BSP Firmware		*No/No*	*No*	*Firmware BSP*	*None available to user*
-FPGA Config		*Yes/No*	*No*	*FPGA Startup*	*Clear Startup App*
(9636)					
SSD	*512 MB*	*Yes/Yes*	*No*	*Primary Storage*	*Reformatting the drive*
-BSP Firmware		*No/No*	*No*	*Firmware BSP*	*None available to user*
-FPGA Config		*Yes/No*	*No*	*FPGA Startup*	*Clear Startup App*

Media Storage

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing

NONE

Clearing Notes:

Terms and Definitions

User Accessible User accessible memory allows the user to directly write or modify the contents of the memory during normal instrument operation.

System Accessible System accessible memory does not allow the user to access or modify the memory during normal instrument operation, however, may be accessed or modified by background processes. This can be something that is not deliberate by the user and can be a background driver implementation, such as storing application information in RAM to increase speed of use.

Cycle Power The process of completely removing power from the device and its components. This includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

Volatile Memory Volatile memory requires power to maintain the stored information. When power is removed from this memory, its contents are lost.

Non-Volatile Non-volatile memory retains its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.