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**cRIO-9064**

**Board Assembly Part Number(s)**

Part Number	Description
156839A-01L or later	cRIO-9064

**Manufacturer:** National Instruments

**Volatile Memory**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing <sup>1</sup>
DRAM	4Gb	No/Yes	No	System Memory	Cycle power
FPGA w/ Block RAM	560KB	Yes/Yes	No	LabVIEW and User data	Cycle power
CPLD	32 bytes	No/Yes	No	CPLD Memory	Cycle power
RTC	20 bytes	No/Yes	Yes	Time Keeping	None available to user

**Non-Volatile Memory**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
CPLD	.17MB	No/No	No	CPLD configuration	None available to user
Flash	1GB				
-Firmware		No/No	No	Firmware	None available to user
-OS		No/Yes	No	Operating System	Format drive in MAX
-User Disk		Yes/Yes	No	User data	Format drive in MAX

**Media Storage**

Type	Size	User Accessible/ System Accessible	Battery Backup?	Purpose	Method of Clearing
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NONE

<sup>1</sup> The designation *None Available to User* indicates that the ability to clear this memory is not available to the user under normal operation. The utilities required to clear the memory are not distributed by National Instruments to customers for normal use.

**Terms and Definitions**

**User Accessible** Allows the user to directly write or modify the contents of the memory during normal instrument operation.

**System Accessible** Does not allow the user to access or modify the memory during normal instrument operation. However, system accessible memory may be accessed or modified by background processes. This can be something that is not deliberate by the user and can be a background driver implementation, such as storing application information in RAM to increase speed of use.

**Cycle Power** The process of completely removing power from the device and its components. This process includes a complete shutdown of the PC and/or chassis containing the device; a reboot is not sufficient for the completion of this process.

**Volatile Memory** Requires power to maintain the stored information. When power is removed from this memory, its contents are lost.

**Non-Volatile** Retains its contents when power is removed. This type of memory typically contains calibration or chip configuration information, such as power up states.